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000530  
LERNER, DAVID, LITTENBERG,  
KRUMHOLZ & MENTLIK  
600 SOUTH AVENUE WEST  
WESTFIELD, NJ 07090

CONFIRMATION NO. 5265  
FILING RECEIPT



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**Applicant(s)**

Peter Poechnueller, Unterhaching, GERMANY;

**Assignment For Published Patent Application**

Infineon Technologies North America Corp., San Jose, CA;

**Domestic Priority data as claimed by applicant**

**Foreign Applications**

If Required, Foreign Filing License Granted: 10/08/2003

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**Title**

Structure and method of multiplexing bitline signals within a memory array

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